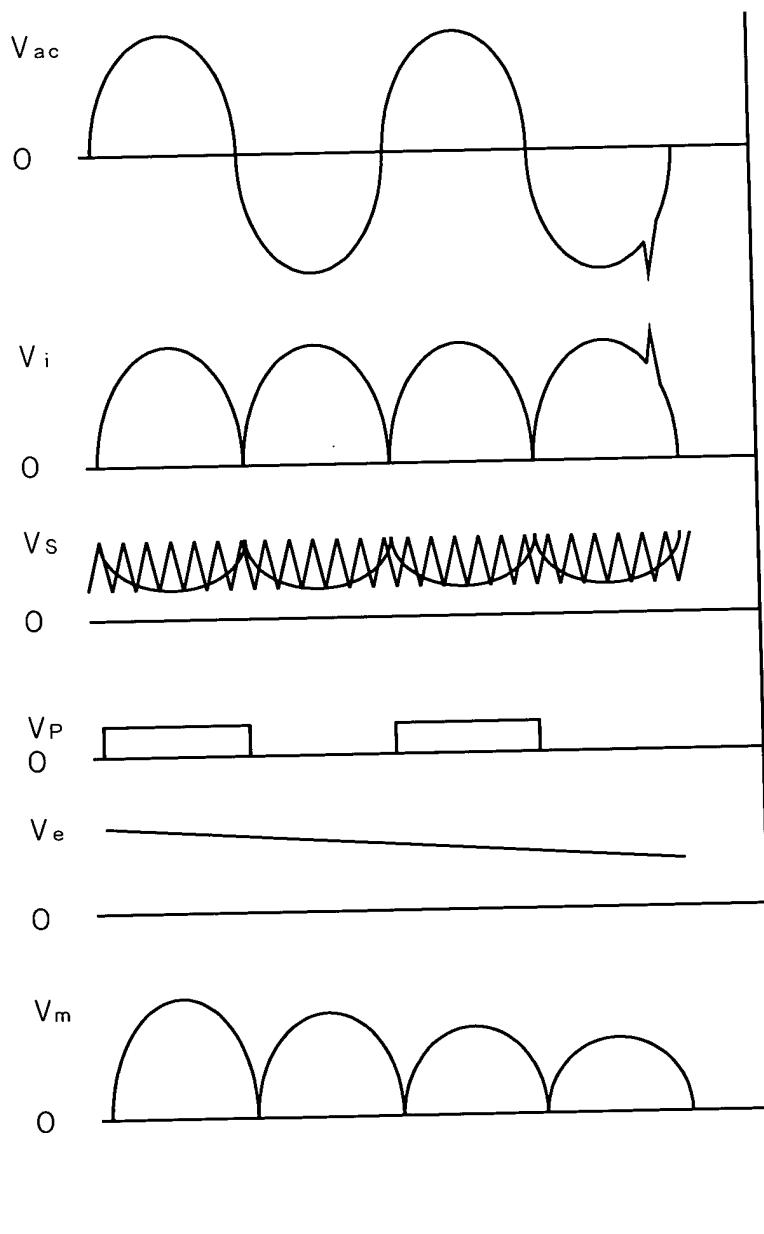
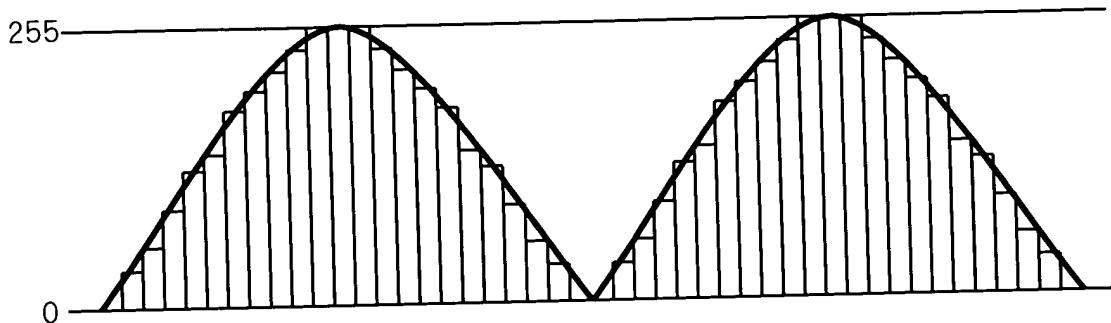


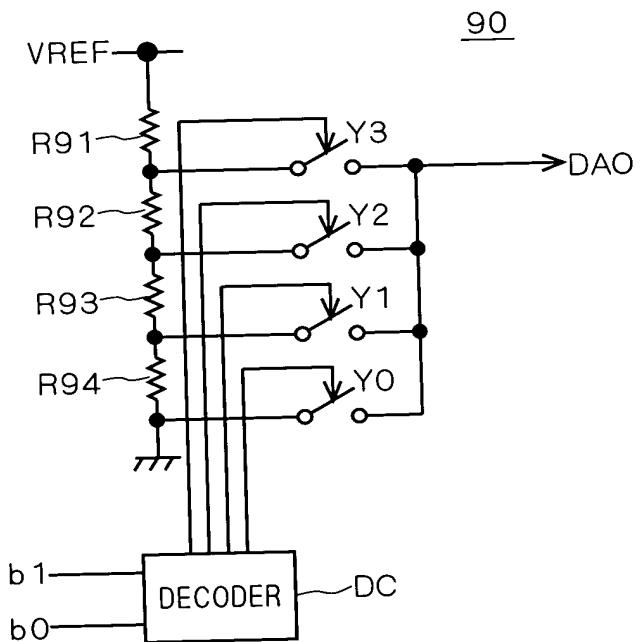
F I G . 2



F I G . 3

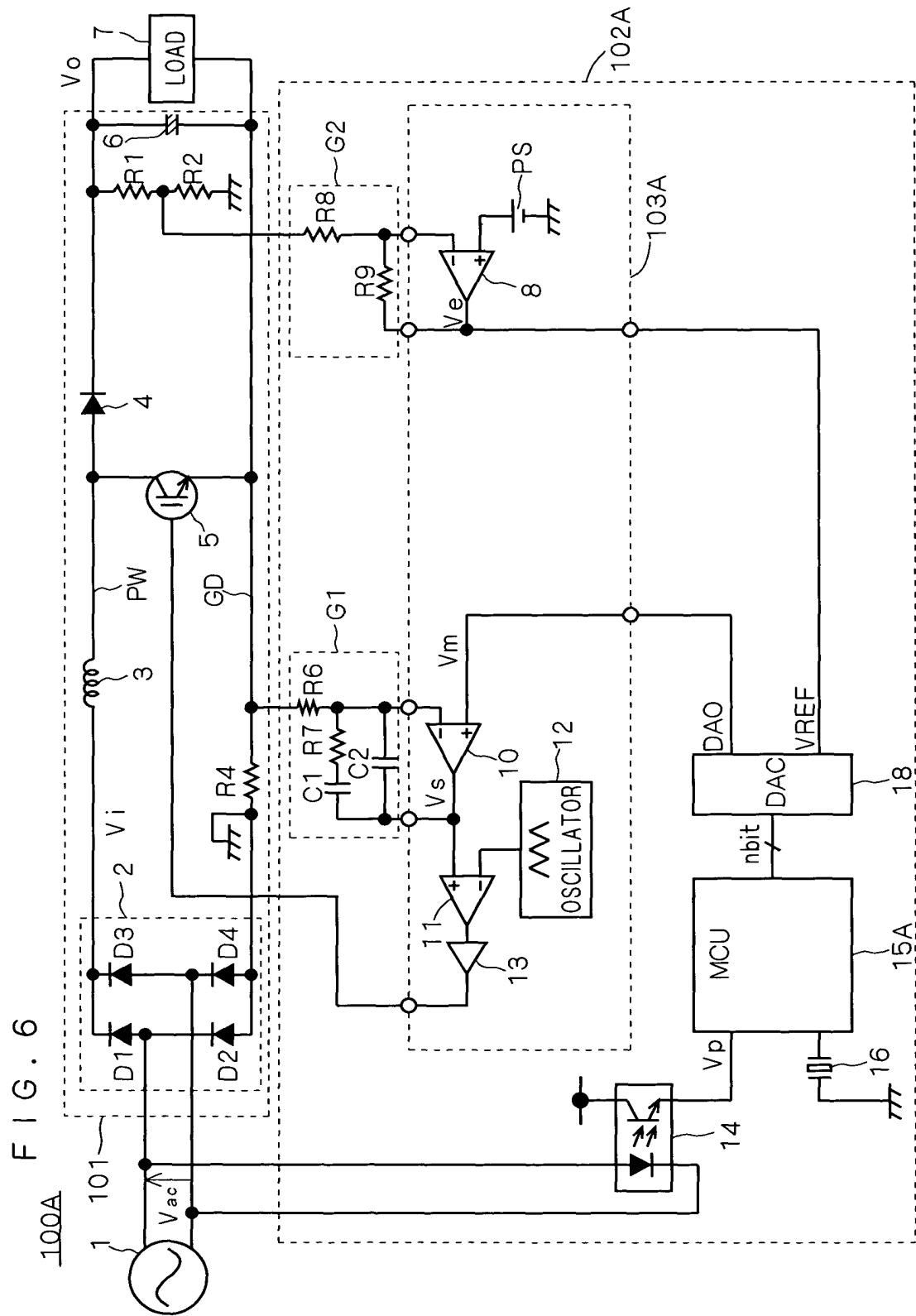


F I G . 4



F I G . 5

b 0	b 1	DECIMAL NUMBER	D A O
0	0	0	0
1	0	1	(1/4) V R E F
0	1	2	(2/4) V R E F
1	1	3	(3/4) V R E F



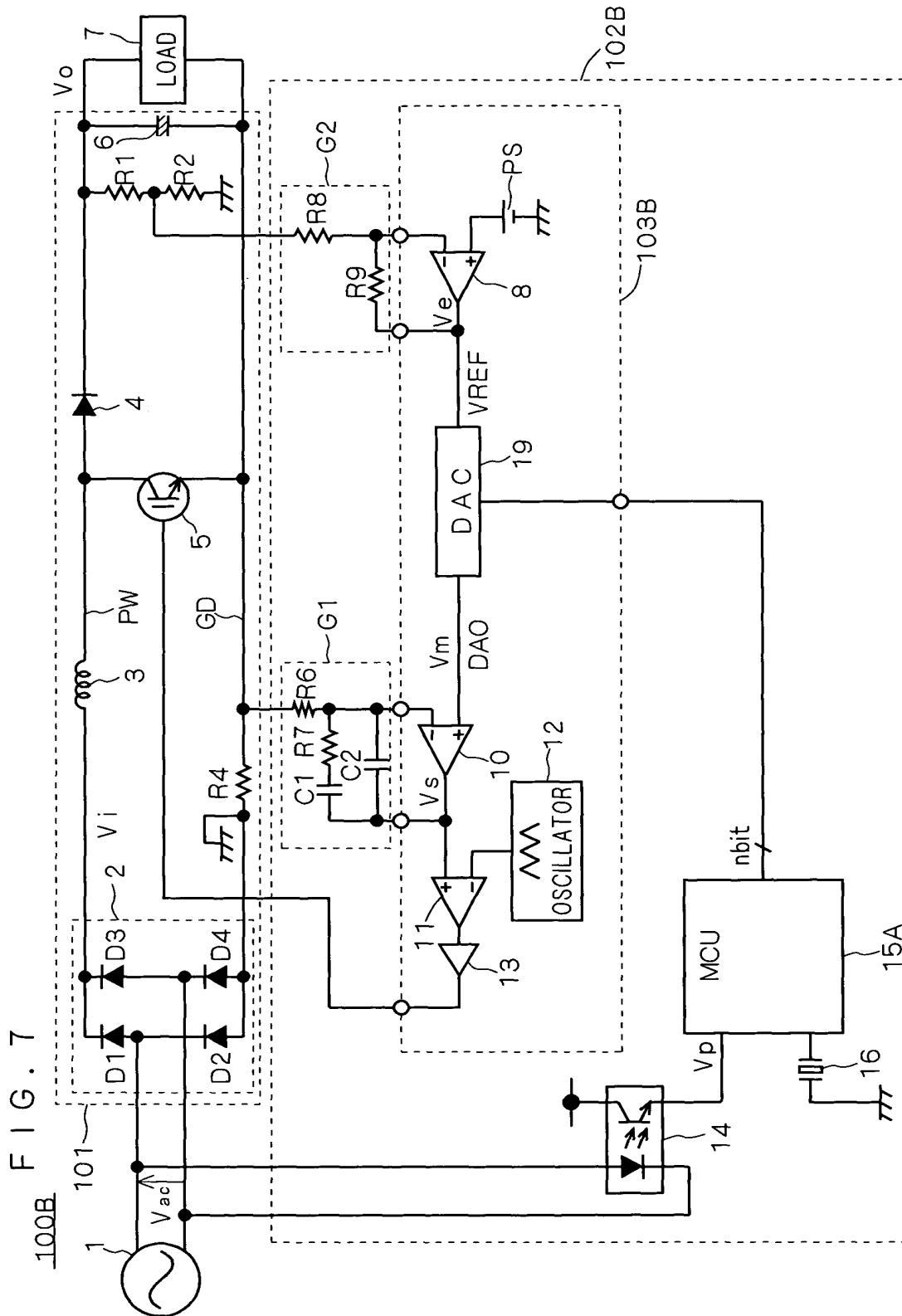
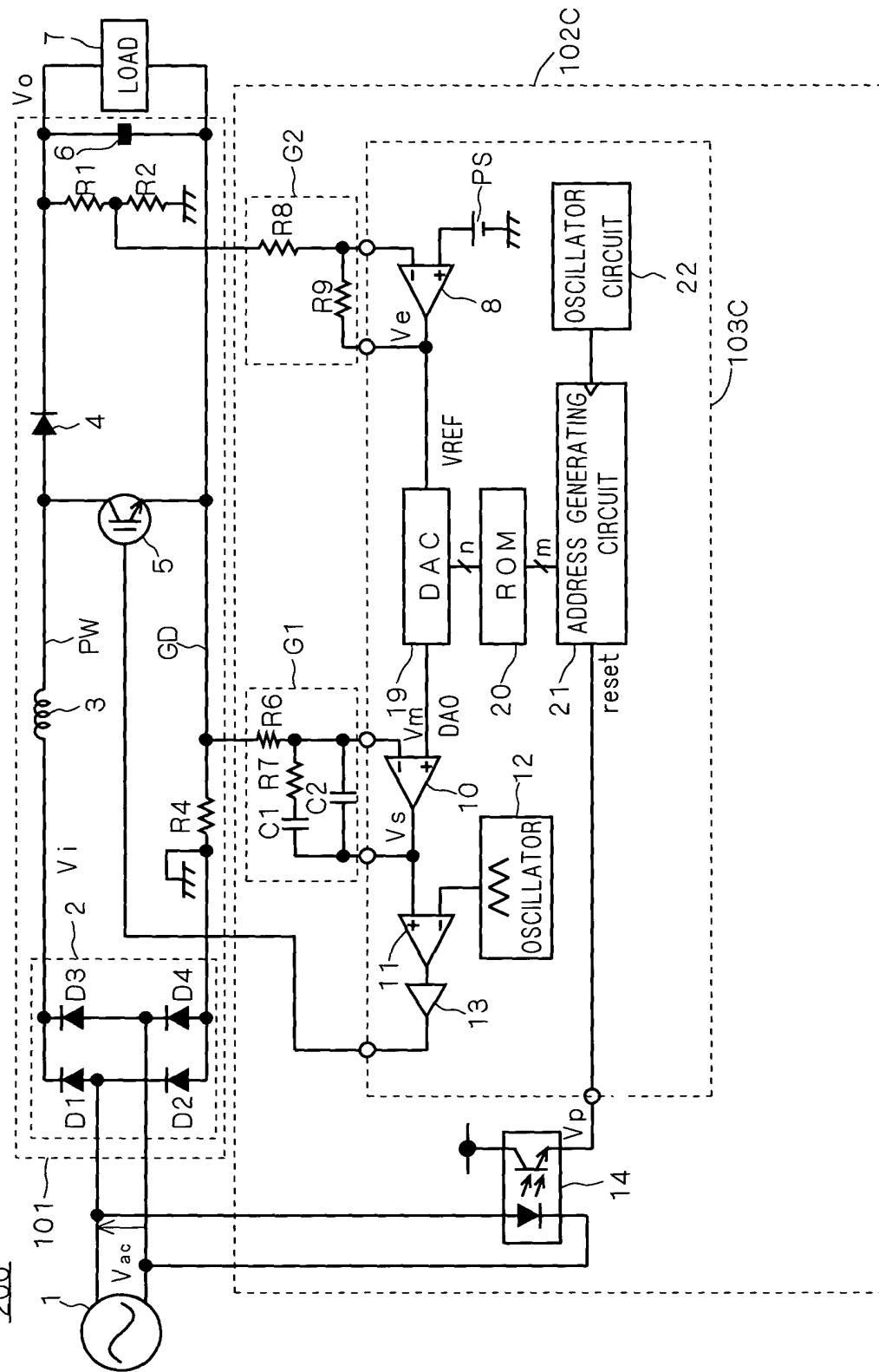
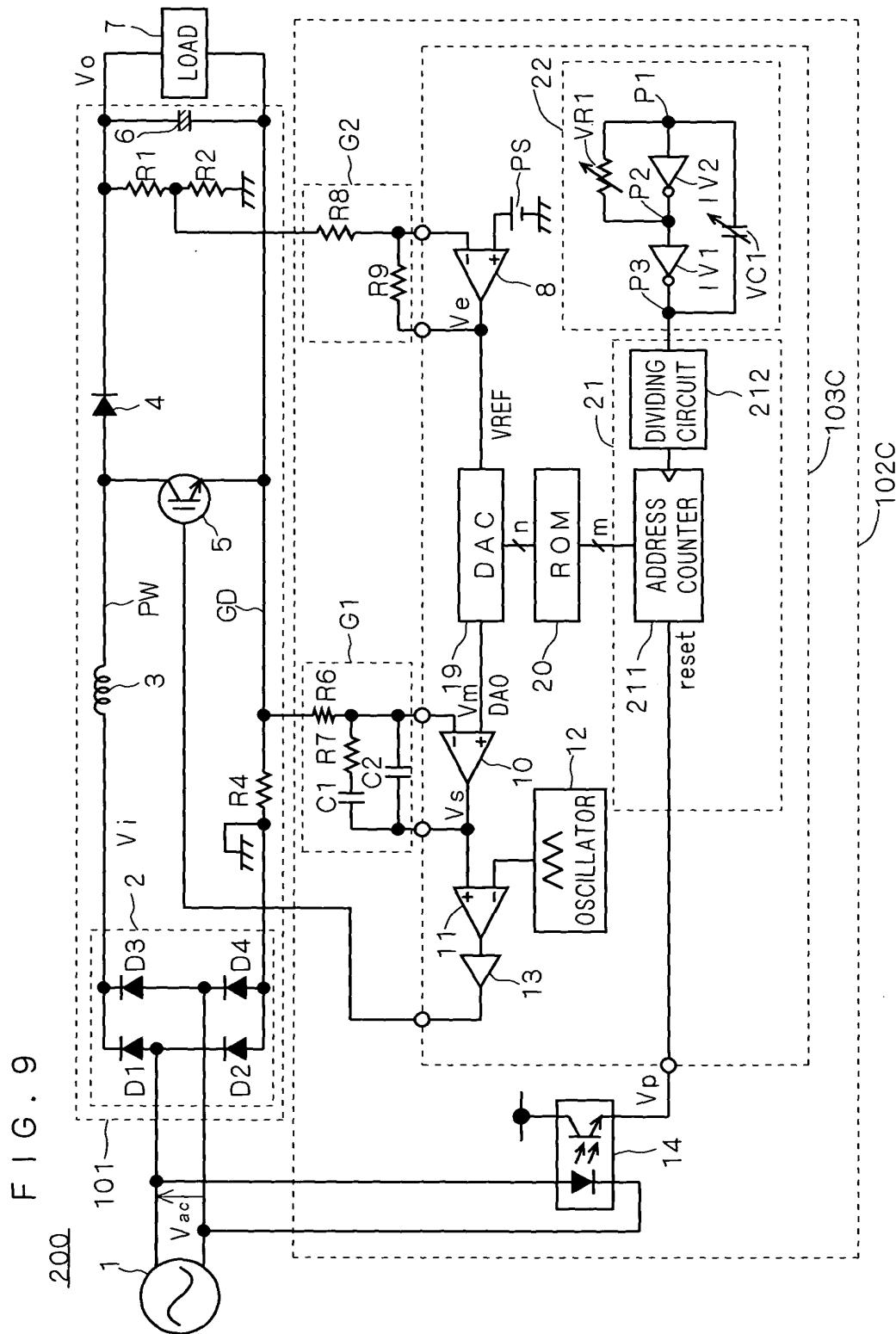


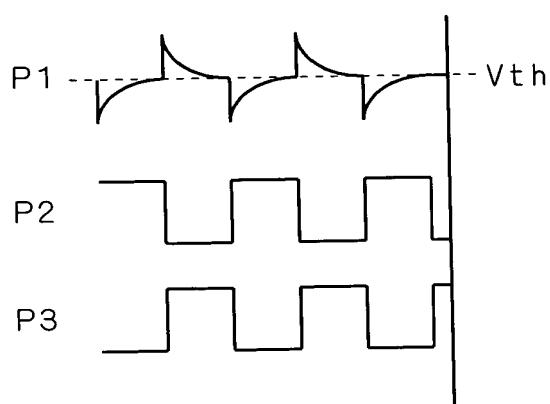
FIG. 8

200





F I G . 1 0



F I G . 1 1

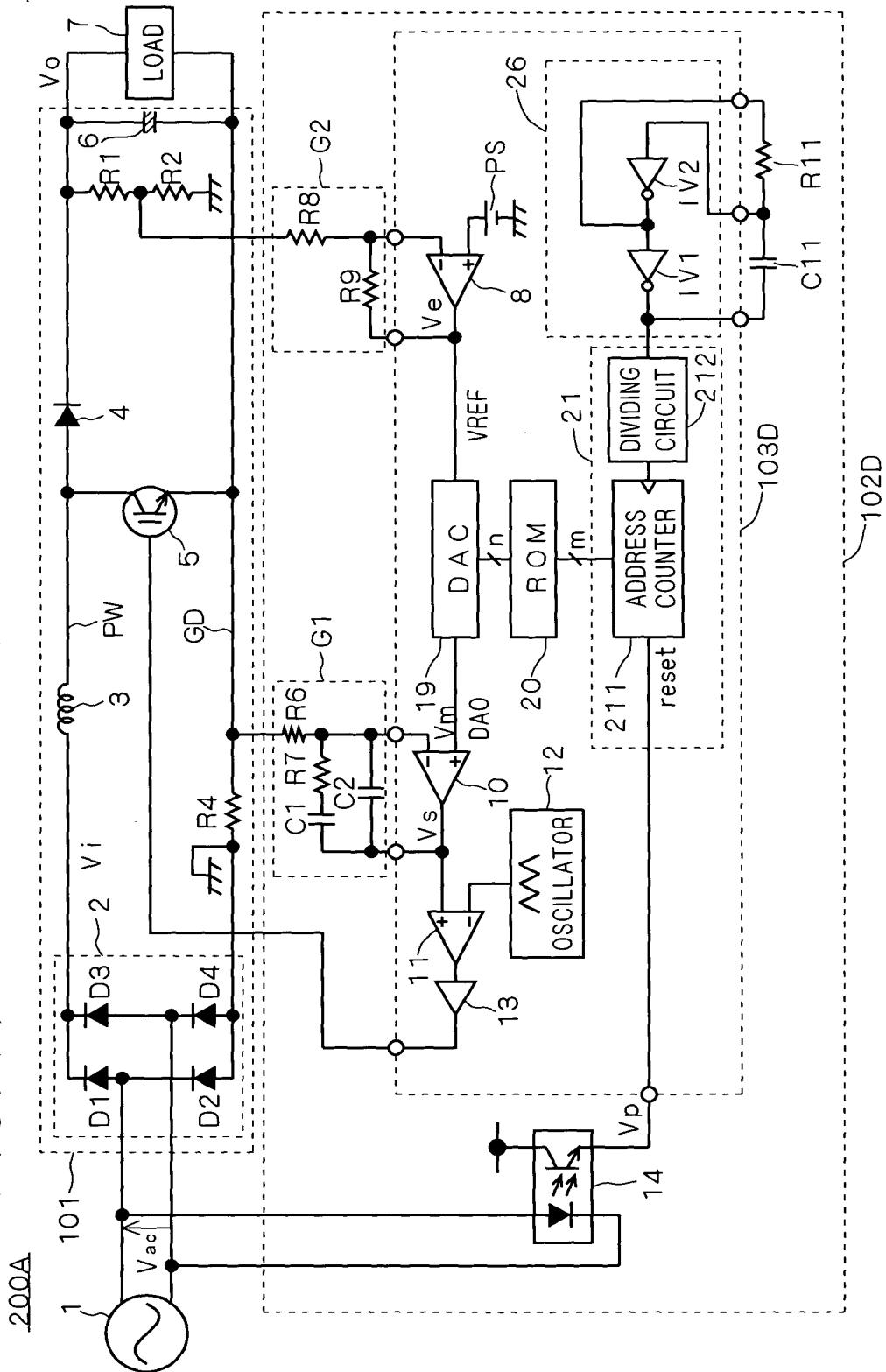


FIG. 12  
 200B

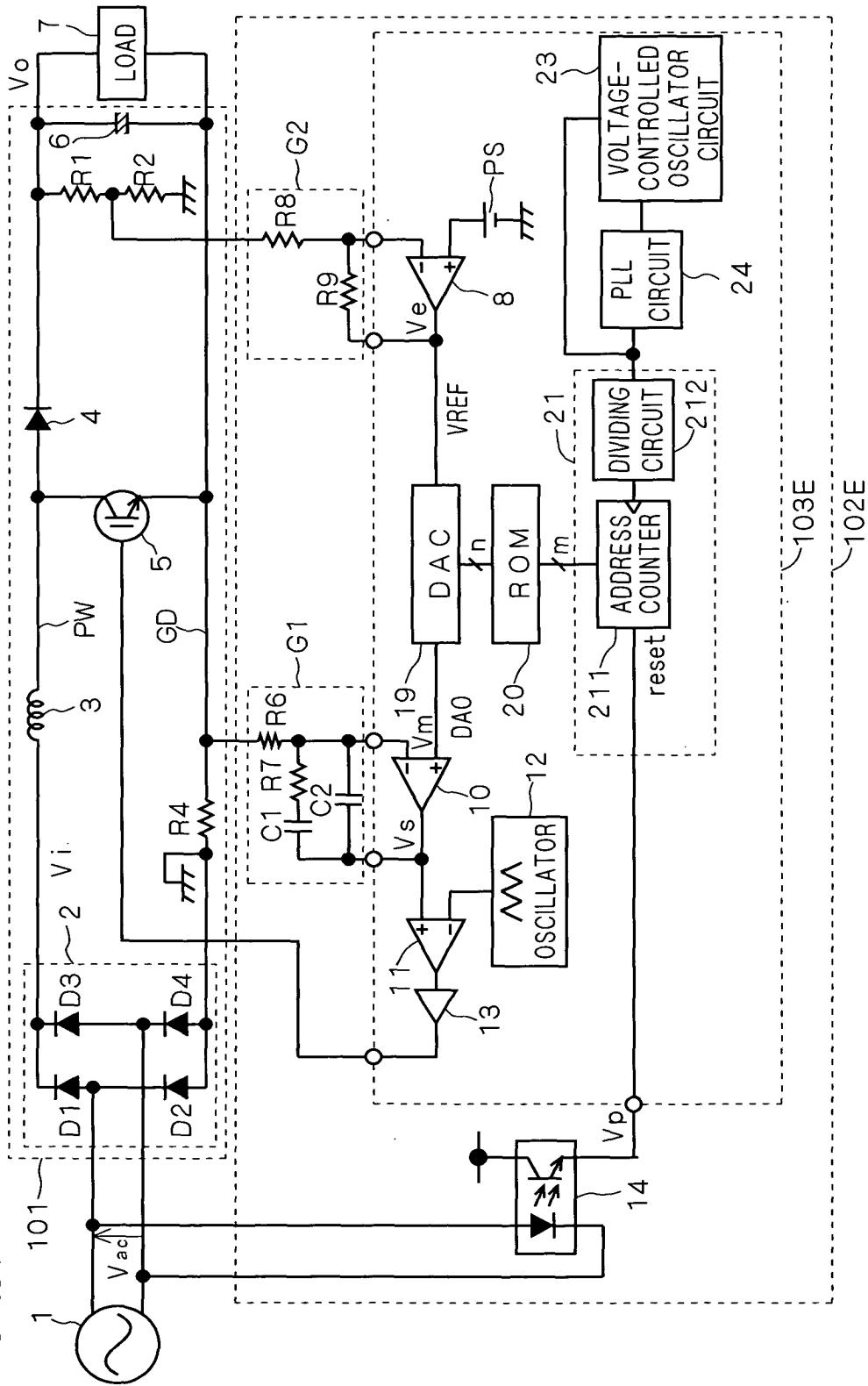


FIG. 13

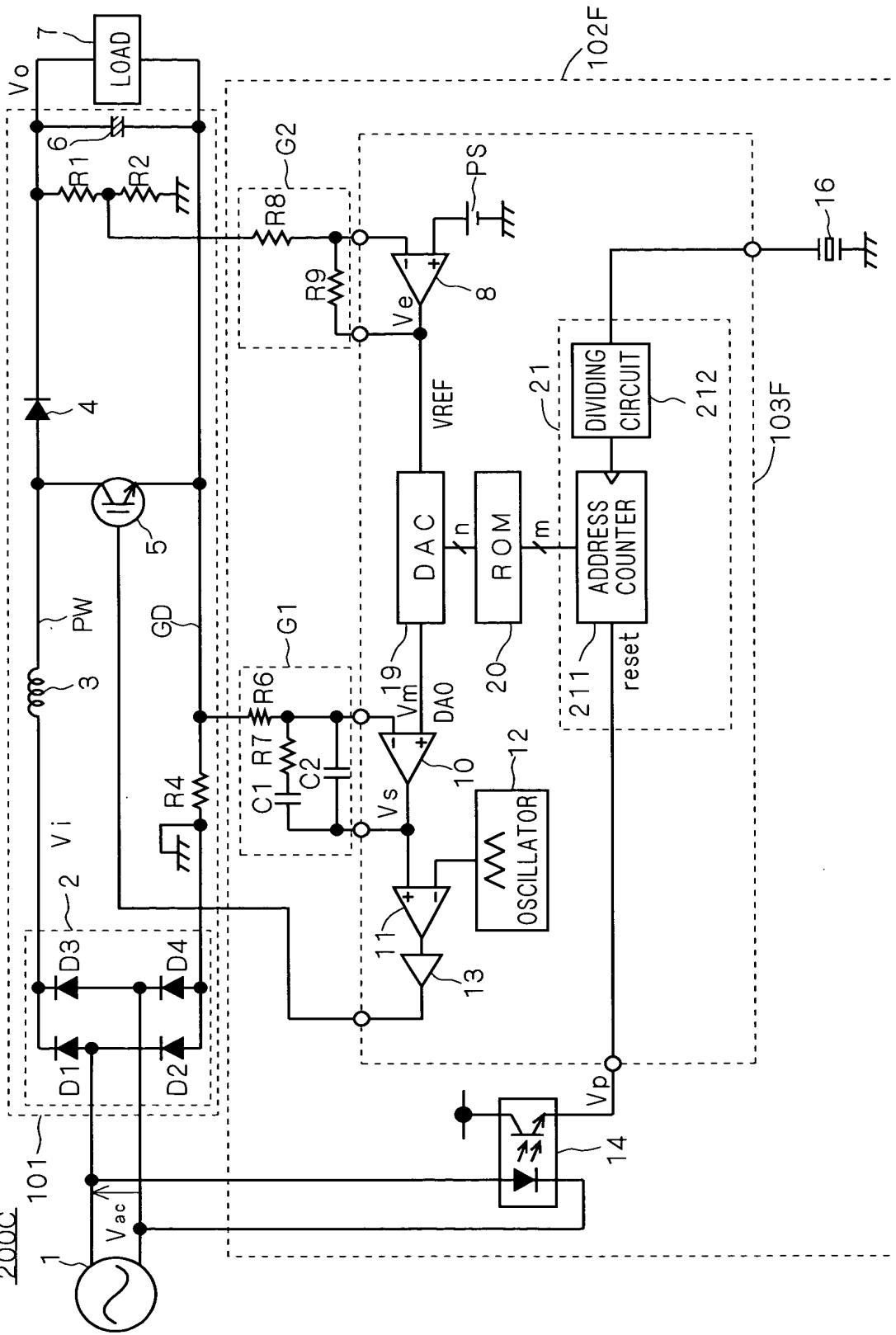
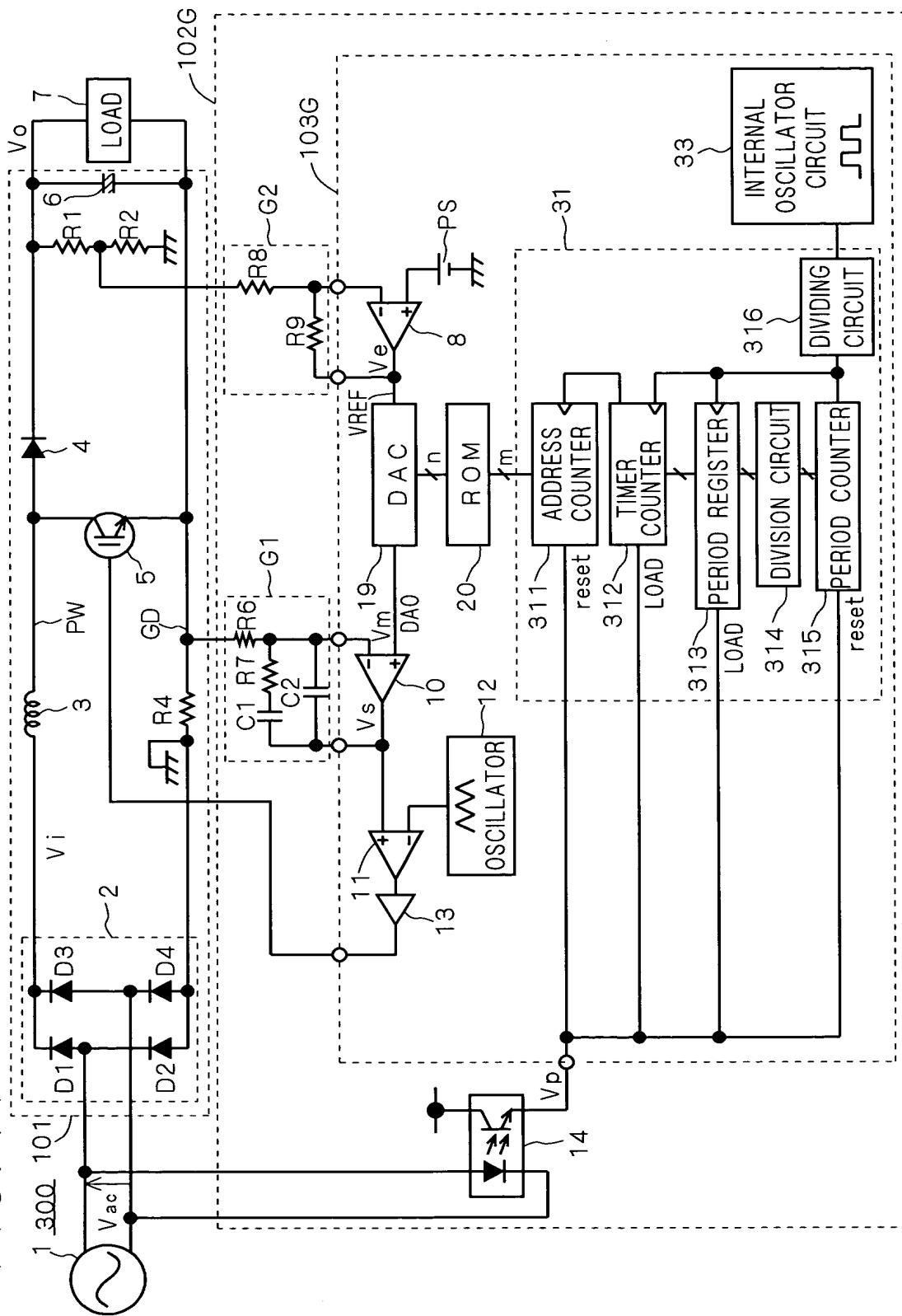
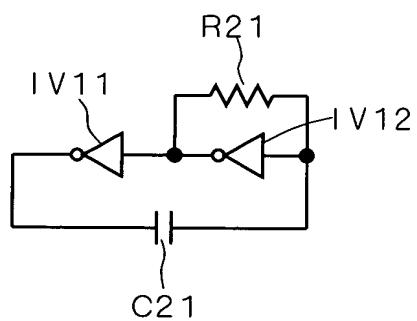
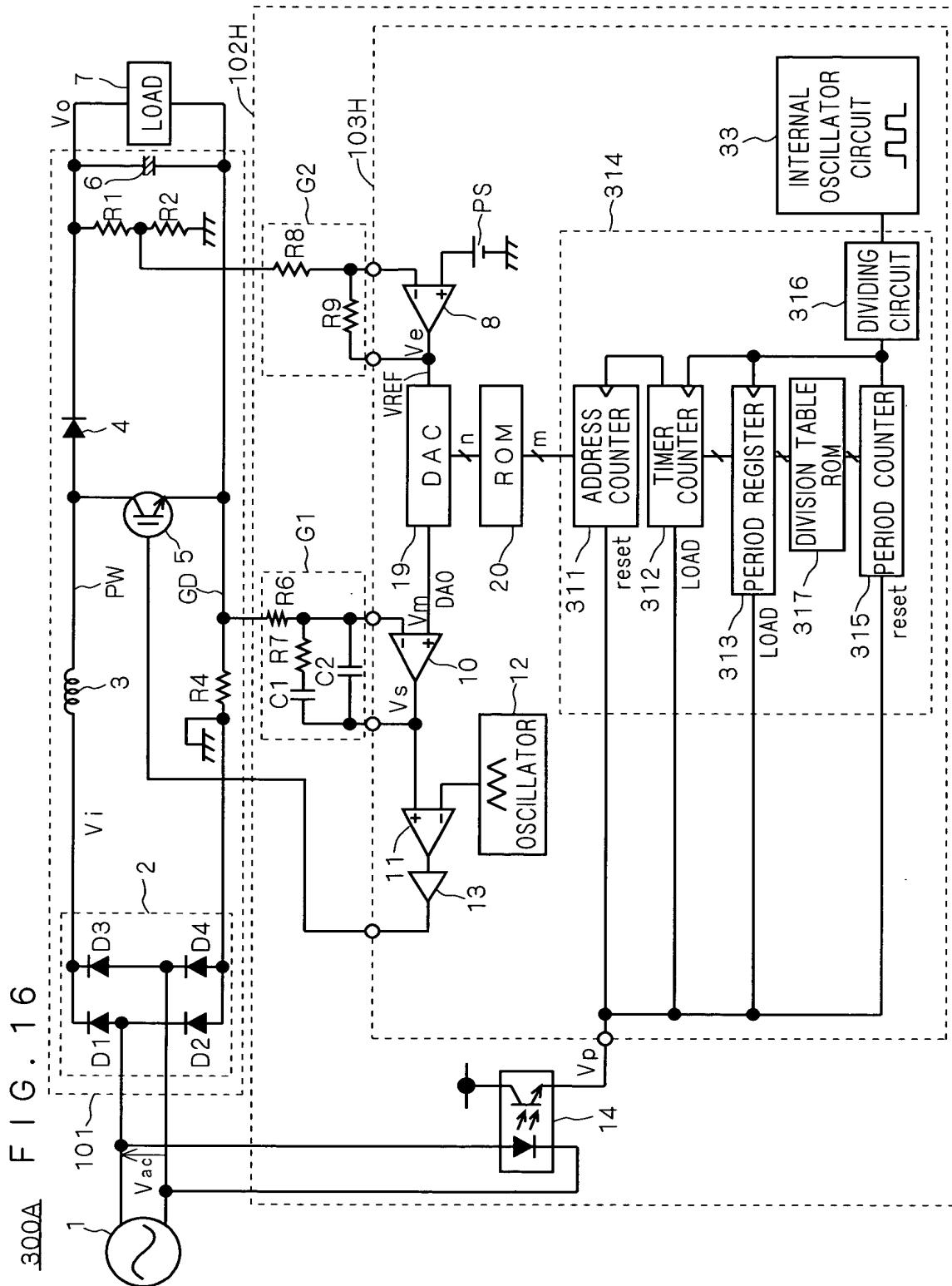


FIG. 14



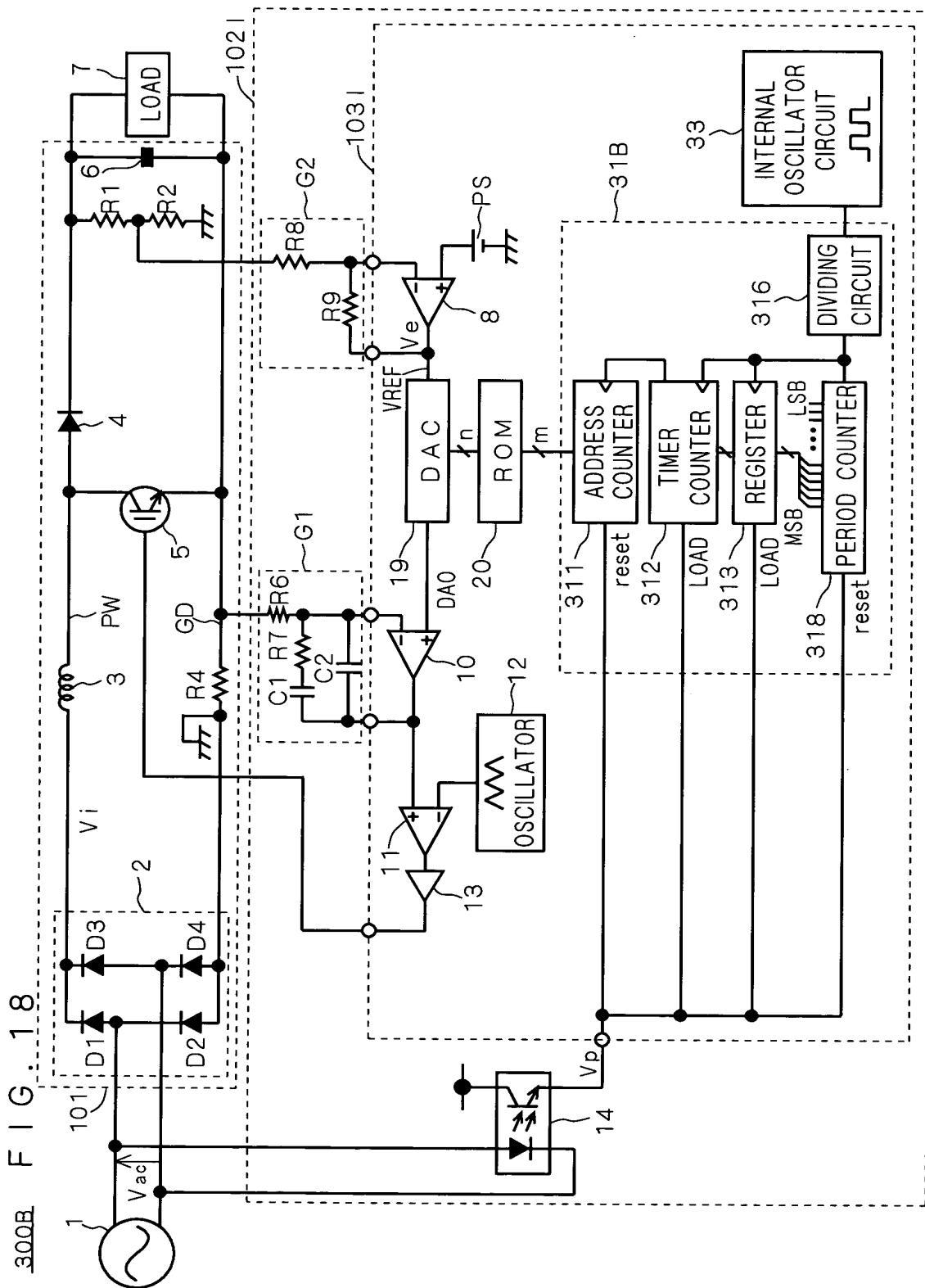
F I G . 1 5





F I G . 1 7

ROM ADDRESS	K 1
0	0
1	0
2	1
3	1
4	2
5	2
6	3
7	3
...	...



F I G . 1 9

	BINARY								DECIMAL								
	MSB				LSB												
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0	1	1	1	0	0	0	0										
1-BIT RIGHT-SHIFT	0	0	1	1	1	0	0	0	→ 56								
2-BIT RIGHT-SHIFT	0	0	0	1	1	1	0	0	→ 28								